

PATENT
0510-1120

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Pere ROCA I CABARROCAS et al. Conf. 8230

Application No. 10/544,787 Group 2815

Filed May 26, 2006 Examiner Jay C. Kim

TRANSISTOR FOR ACTIVE MATRIX DISPLAY AND A METHOD FOR PRODUCING
SAID TRANSISTOR

RESPONSE

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

December 30, 2008

Sir:

This paper responds to the Official Action mailed
October 30, 2008, for which the period for response is extended
one month:

Remarks begin on page 2 of this paper.